

Атомно-слоевое осаждение для роста соединений AlPbV на кремнии



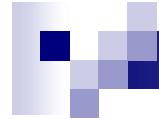
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Outline

- Motivation
- PE-ALD GaP/Si interface properties
- Thermal annealing of PE-ALD GaP/Si interface (RTA, MOCVD)
- First MOCVD growth on PE-ALD GaP/Si templates
- Conclusions

Motivation

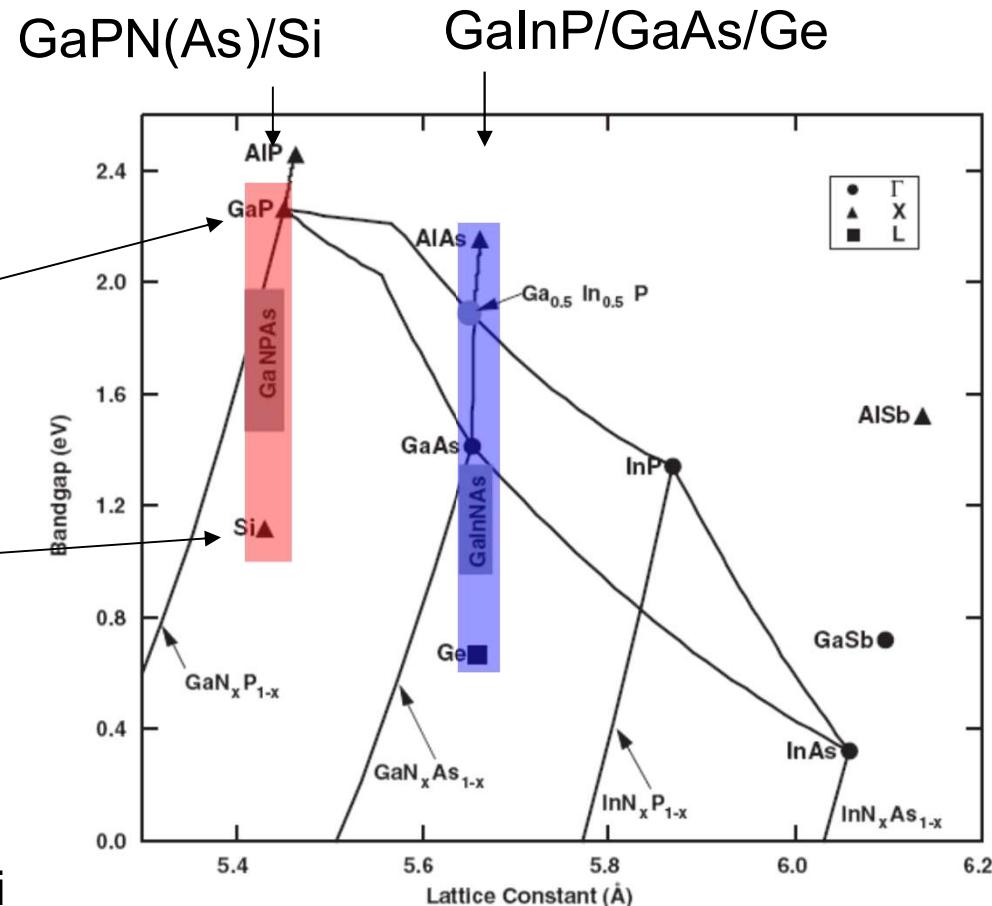
Integration of III-V with Si

Optoelectronic devices
grown on Si

Multijunction solar cells
on Si substrate

GaP (E_g 2.26 eV)
has the smallest lattice mismatch
beyond III-V binary compounds
(<0.4%) to Si

- nucleation layer for III-V multijunction solar cell grown on Si substrate
- appropriate wide gap emitter for Si based sub-cell

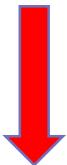


Motivation

Epitaxial growth of GaP requires high temperature

Pre-treatment for Si surface deoxidation and reconstruction **High T 900°C**

Growth **600-750 °C**



**Significant lifetime degradation of Si wafers after annealing in
MOVPE chamber**

R. Varache et al. // Energy Procedia 77 (2015) 493-499

MBE chamber

L. Ding et al. // Energy Procedia 92 (2016) 617



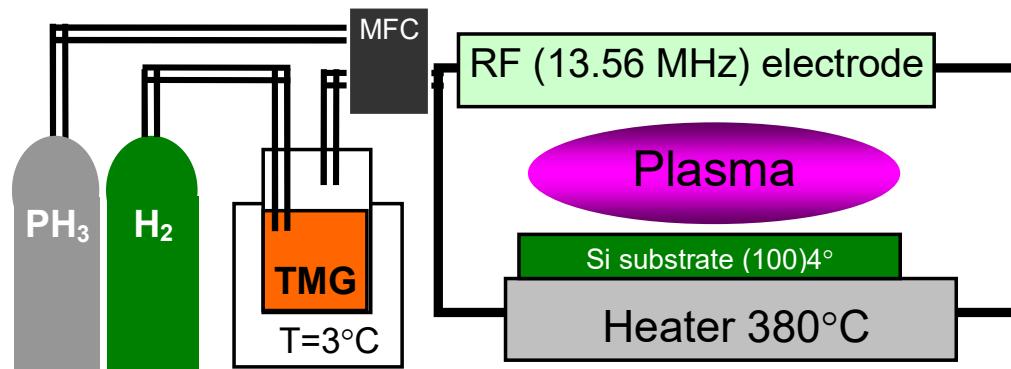
Low temperature nucleation process, which provides 2D growth?



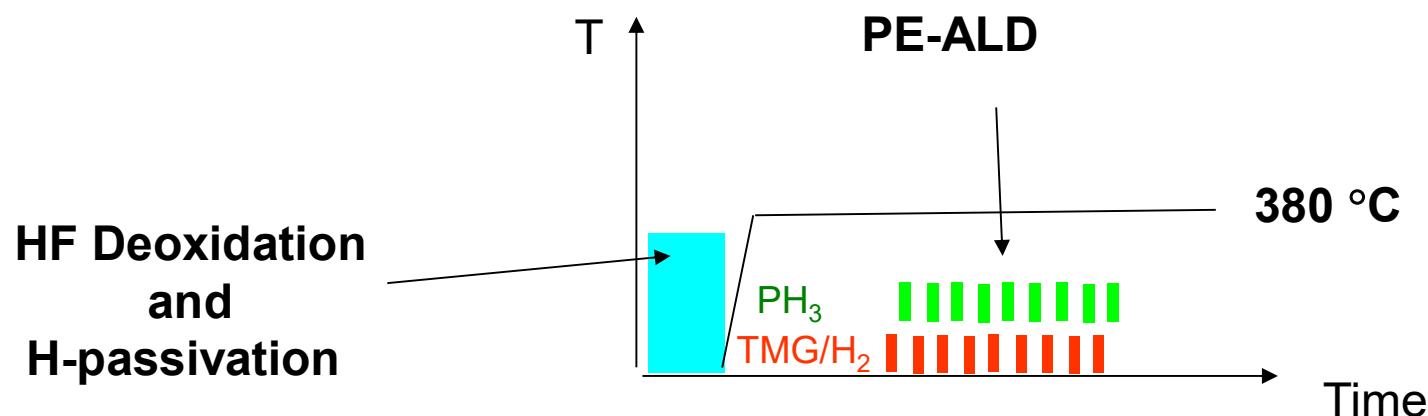
Atomic layer deposition (ALD)

Low Temperature Plasma enhanced atomic layer deposition (PE-ALD)

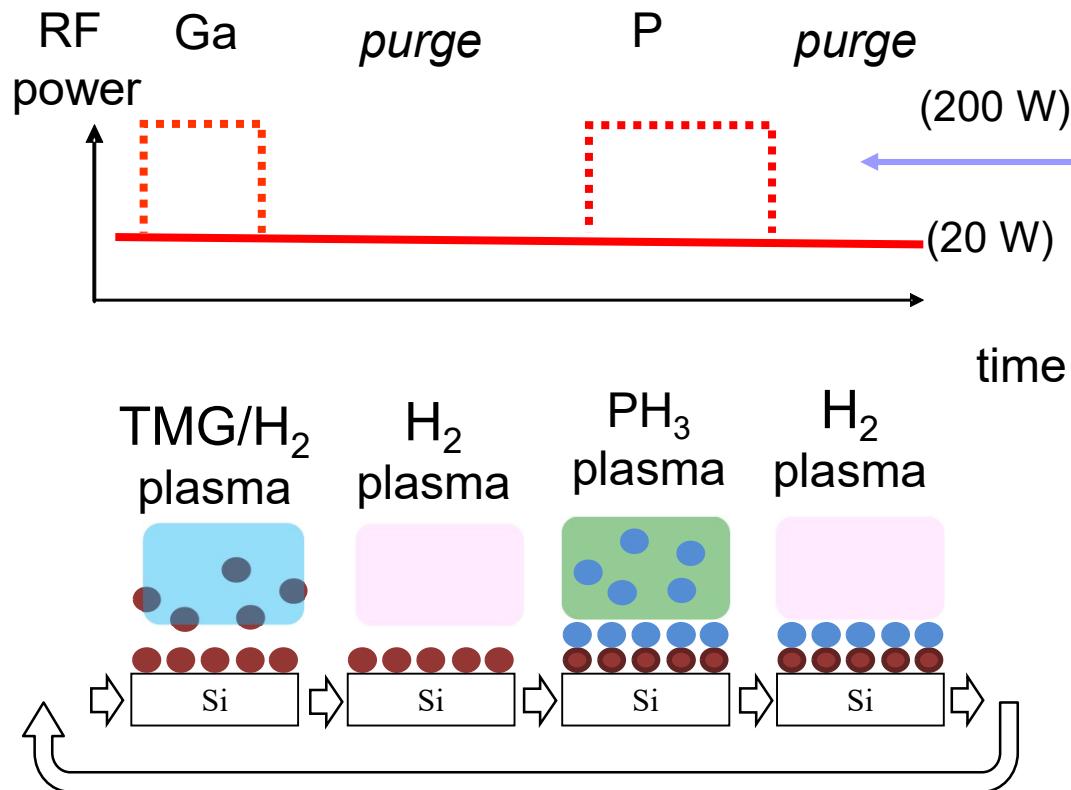
Oxford Plasmalab 100 PECVD



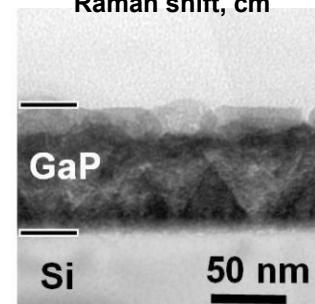
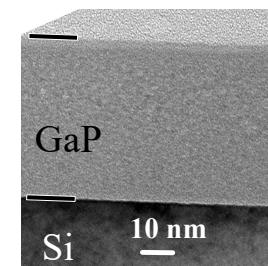
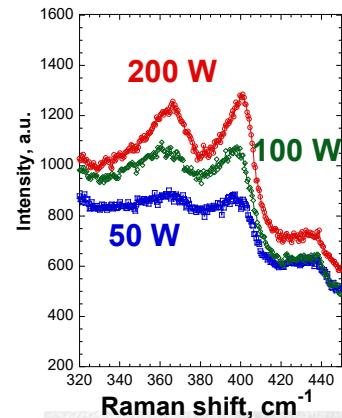
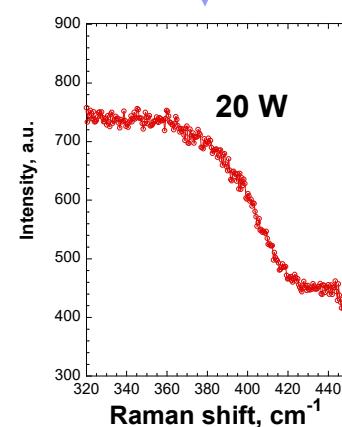
GaP layers were grown on Si (100) 4° cut off substrates at **T=380 °C**



Continues H₂ plasma (pseudo ALD mode)

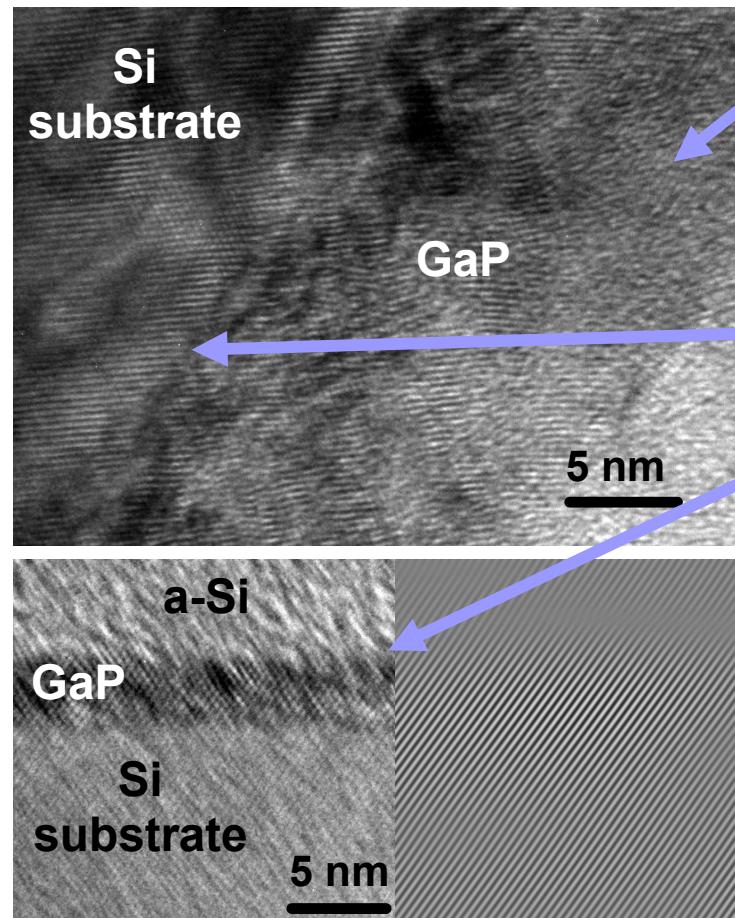


Increase of the RF power pulse
during deposition step
leads to **transfer** from
amorphous to microcrystalline
GaP growth



TEM of GaP/Si interface

H₂-plasma process



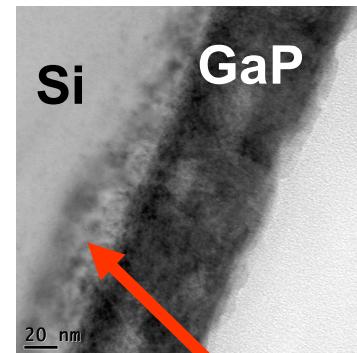
TEM image
of a-Si/epi-GaP/Si

Filtered TEM
image

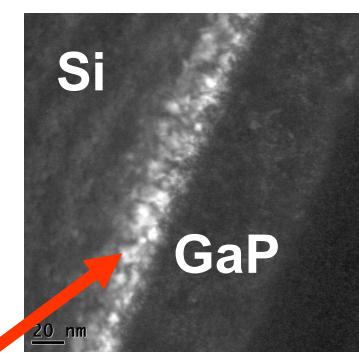
Microcrystalline structure
of GaP films

Epitaxial 2D growth
of 3-5 nm GaP on Si substrate

Bright field TEM

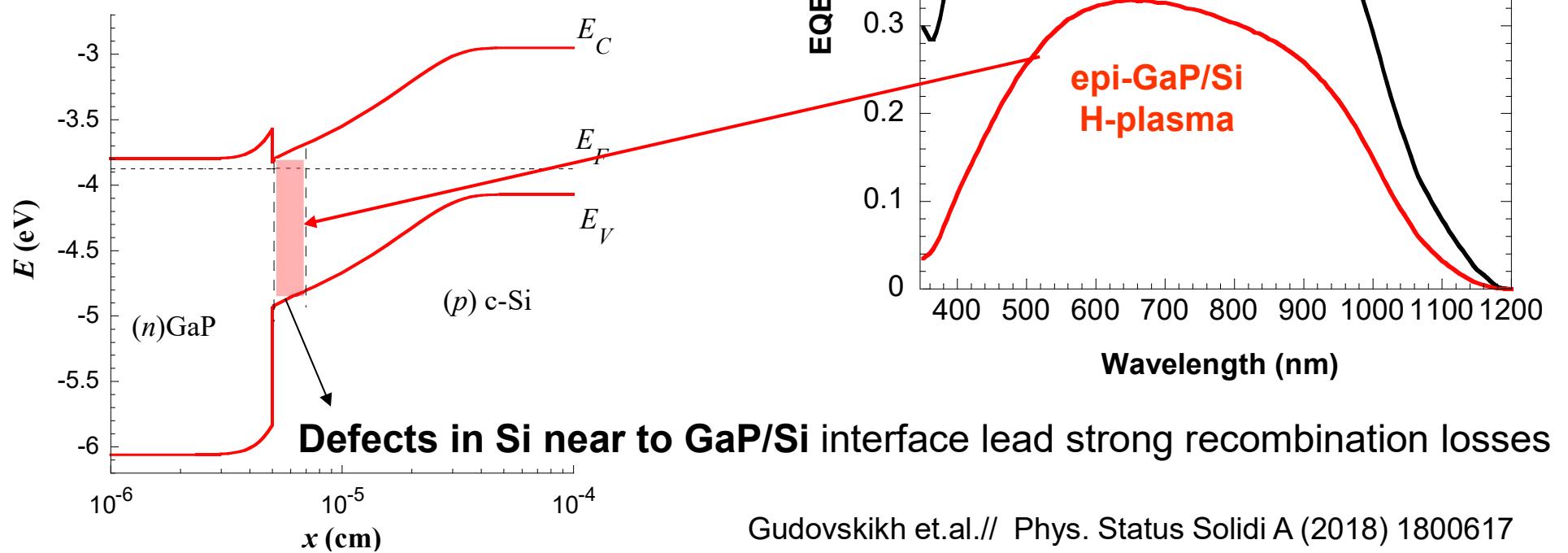
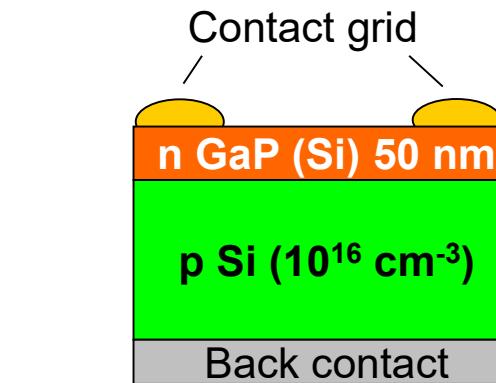


Dark field TEM

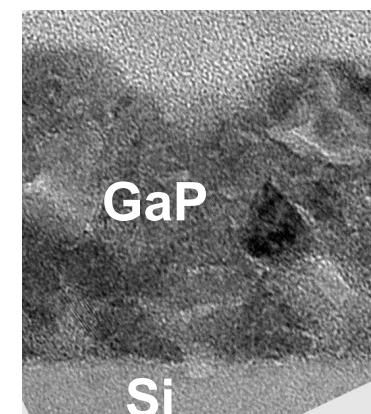
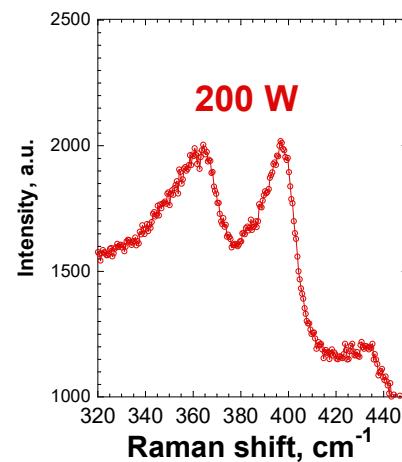
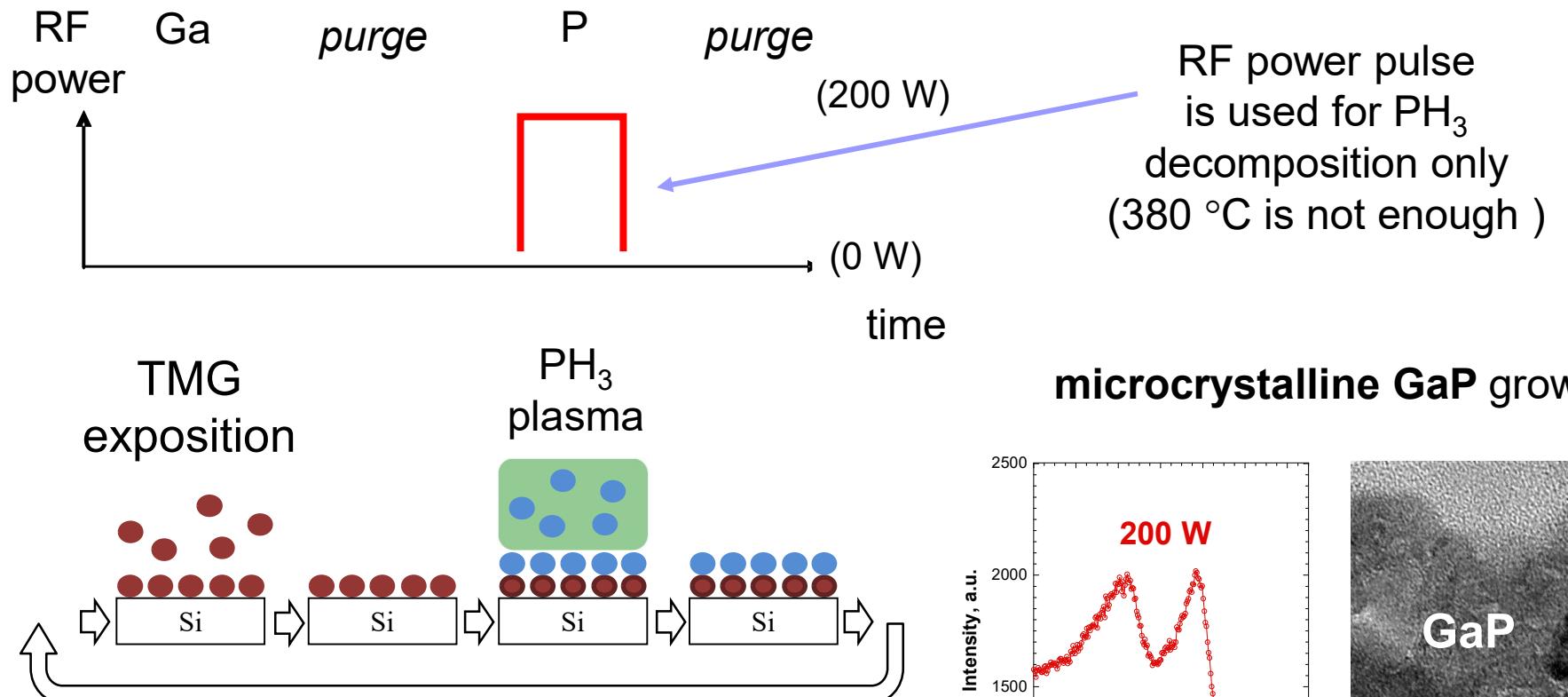


Hydrogen plasma
leads to **damage of Si substrate**
(30-50 nm near to the interface)

Photoelectrical properties of n-GaP/p-Si

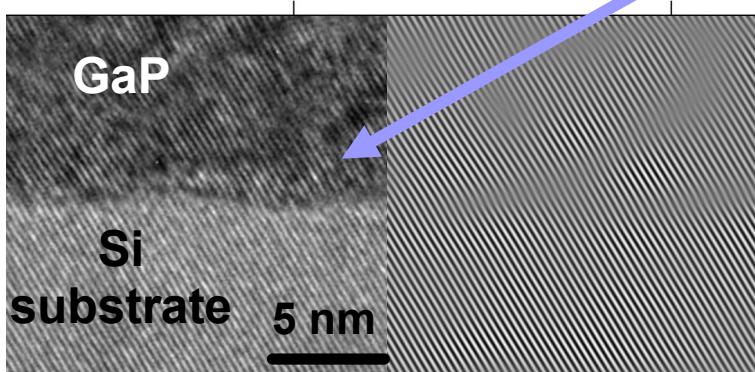
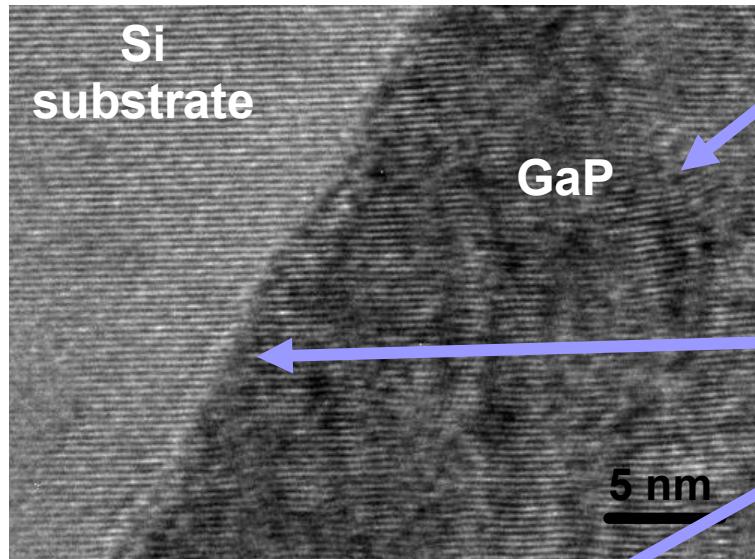


No H₂ plasma (real ALD mode)



TEM of GaP/Si interface

No H₂-plasma process



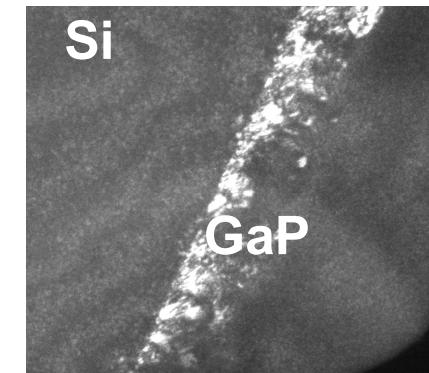
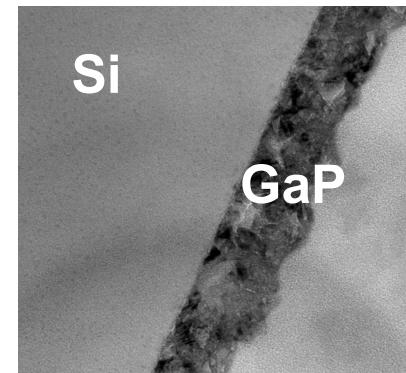
Filtered TEM
image

Microcrystalline structure
of GaP films

Epitaxial 2D growth
of 3-5 nm GaP on Si substrate

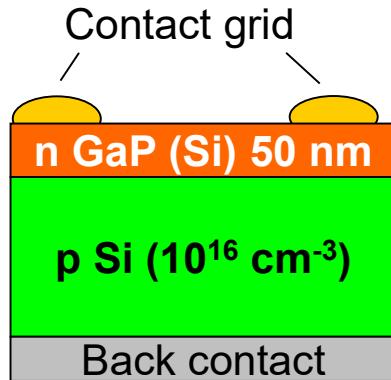
Bright field TEM

Dark field TEM



damage of Si substrate is not
observed by TEM

Photoelectrical properties of n-GaP/p-Si



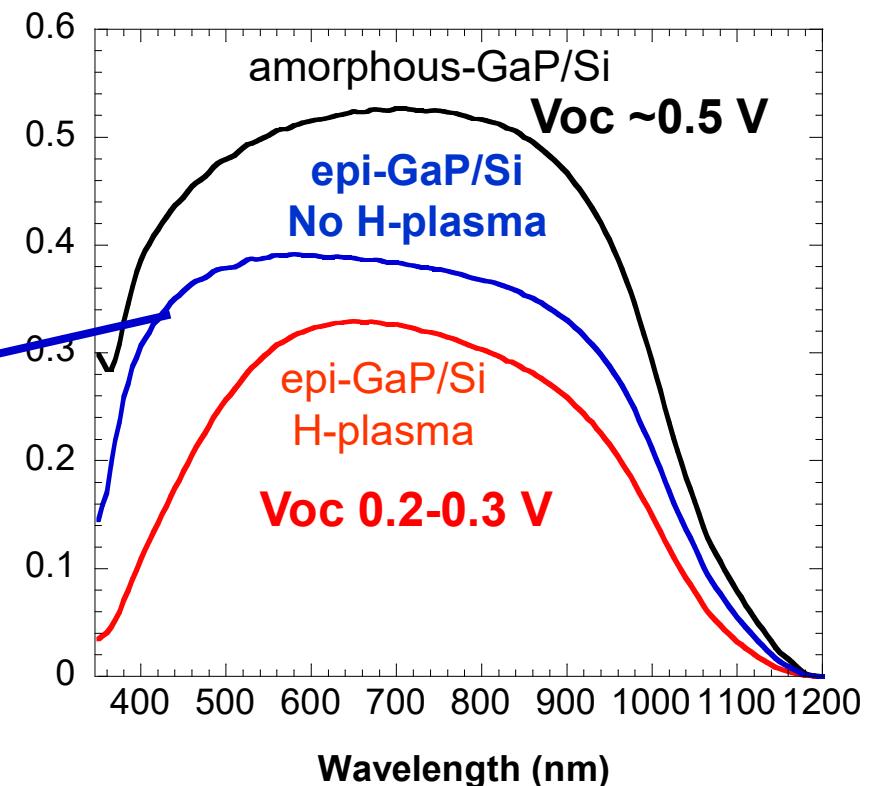
Voc 0.4-0.5 V

Better photoelectrical properties
compared to H-plasma process but still
lower compared to amorphous

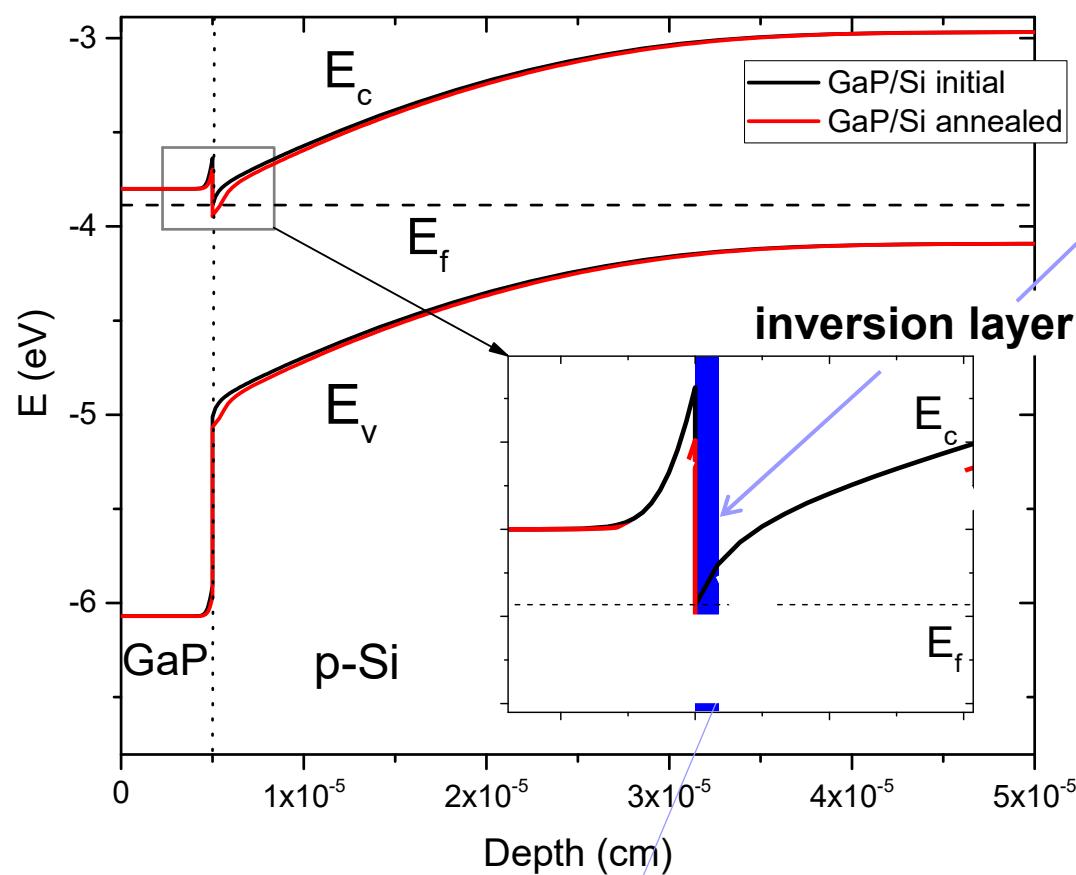


Defects are still created by high power plasma
Presence of hydrogen?

External quantum efficiency

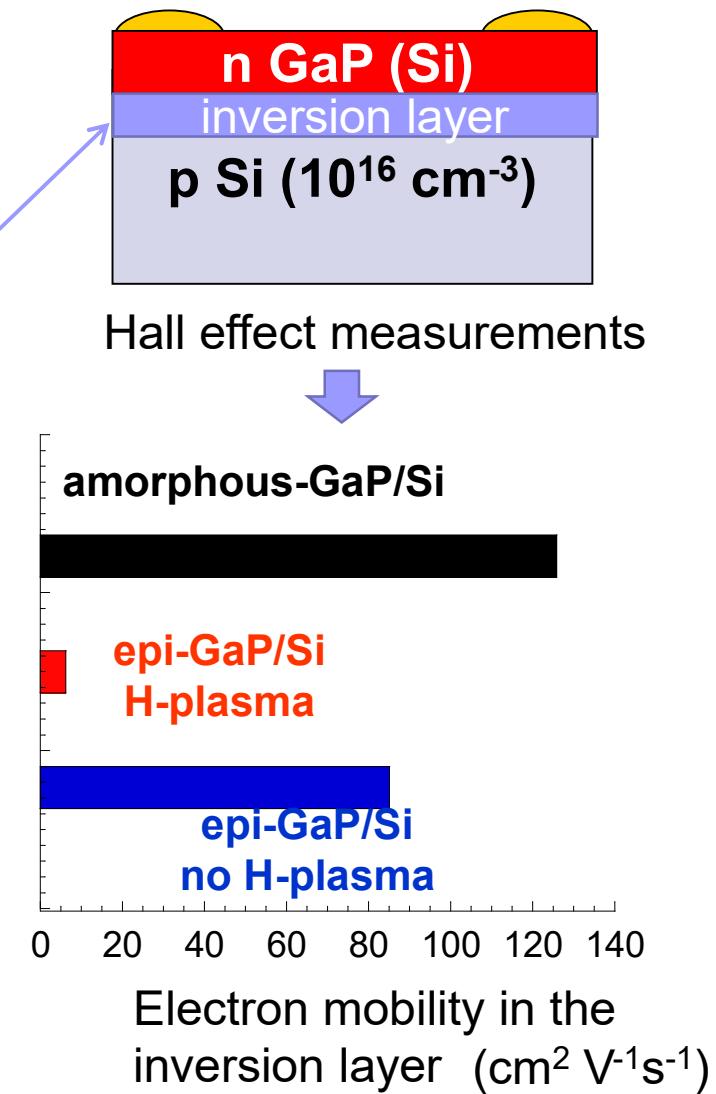


Band diagram of n-GaP/p-Si

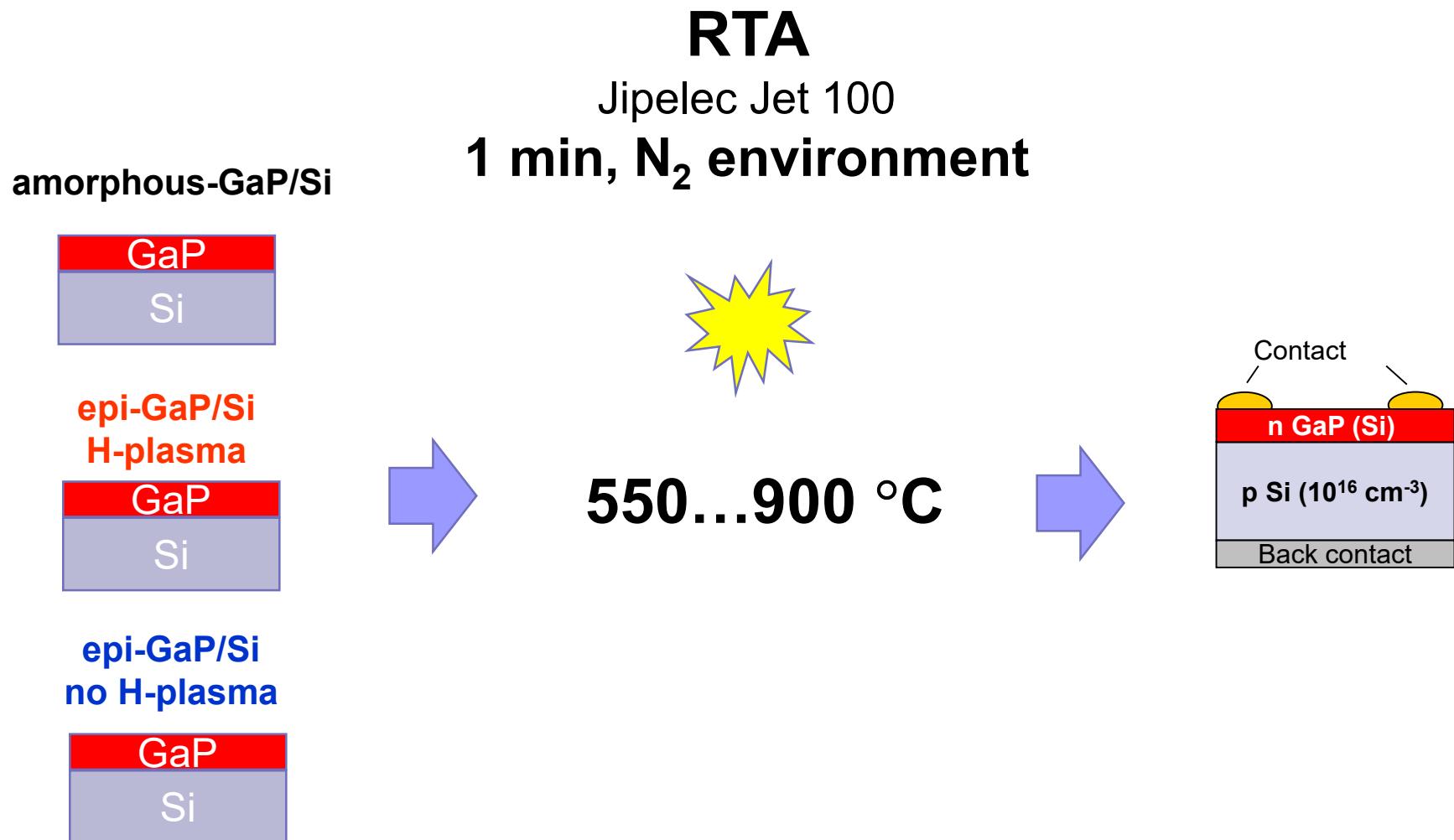


Thermal annealing

- Phosphorous diffusion into p-Si
- Structural changing
- Defects creation/annihilation

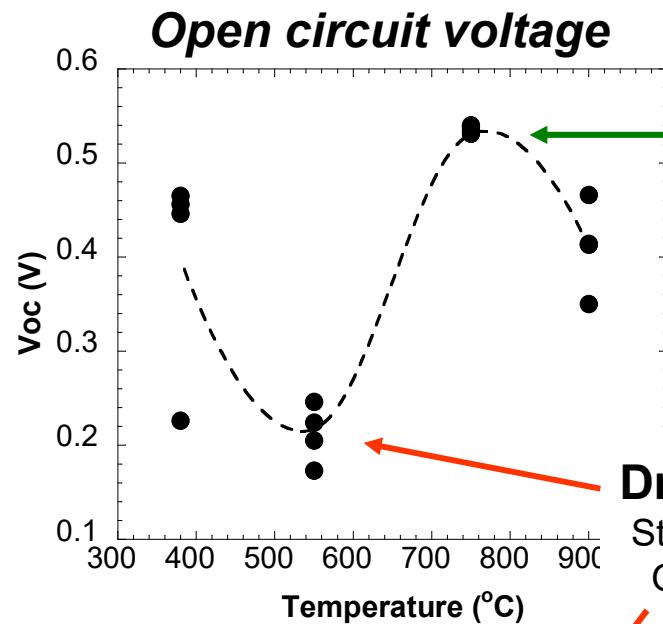


Influence of thermal annealing



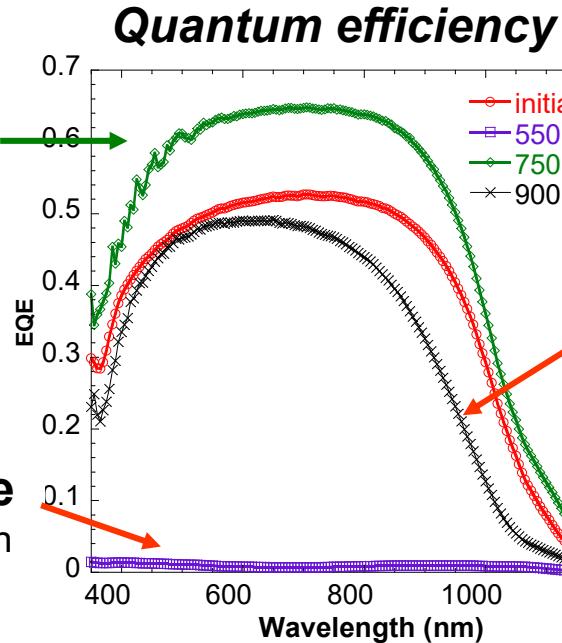


amorphous-GaP/Si

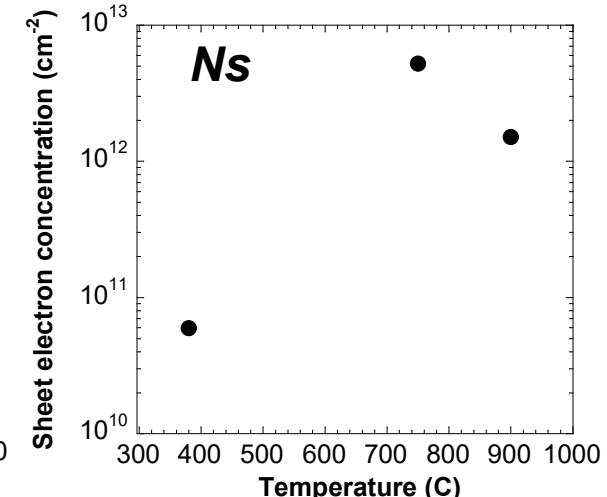
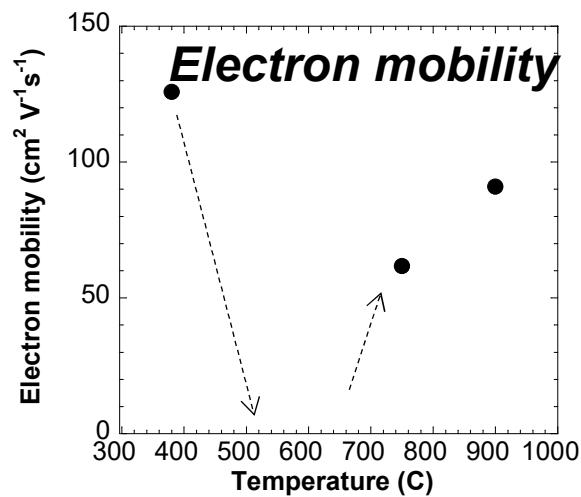
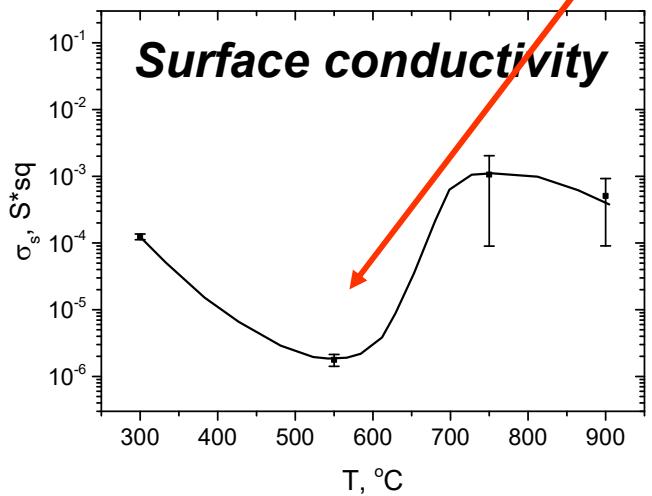


P diffusion
n-p junction in Si

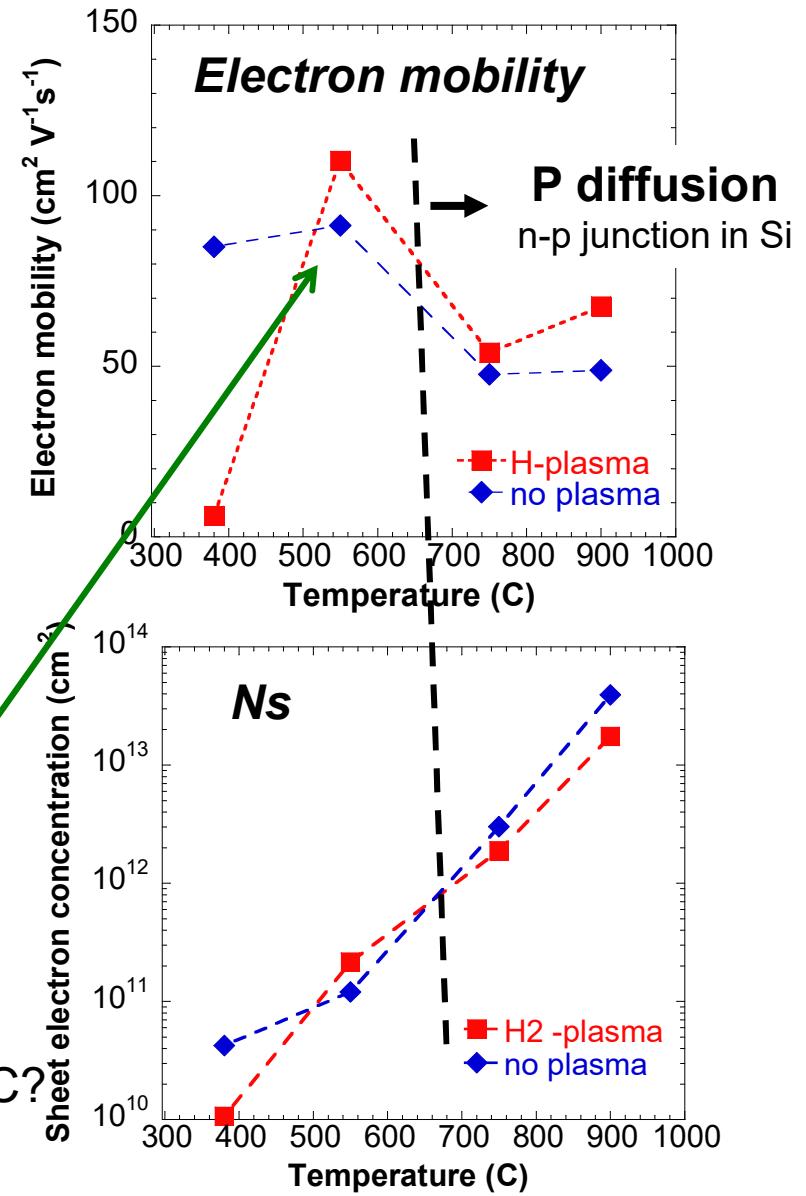
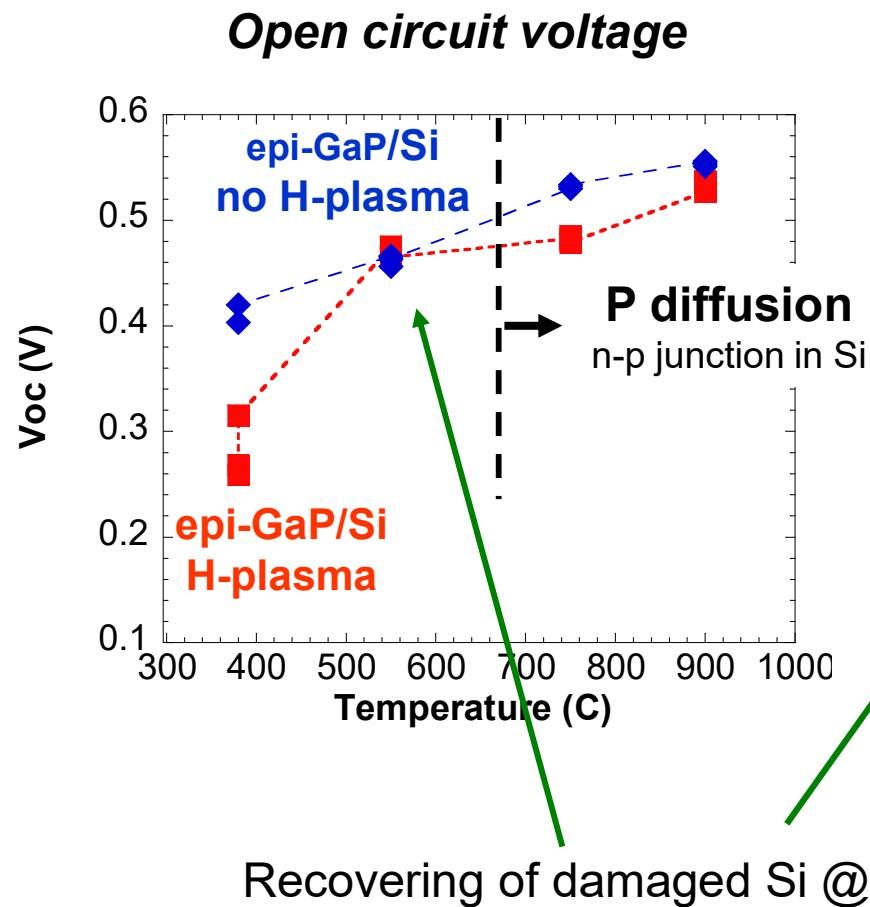
Drastic decrease
Strong Recombination
GaP crystallization?



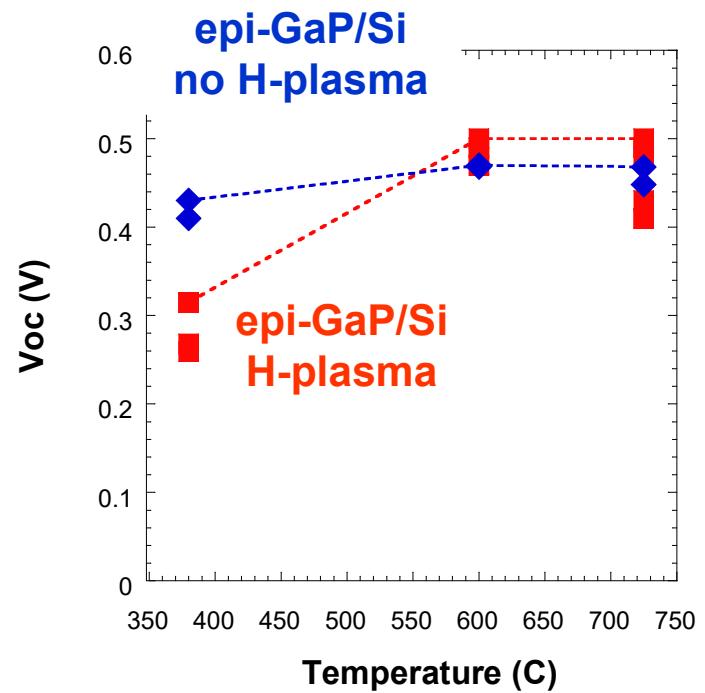
Drop of
"red edge"
Si lifetime
degradation



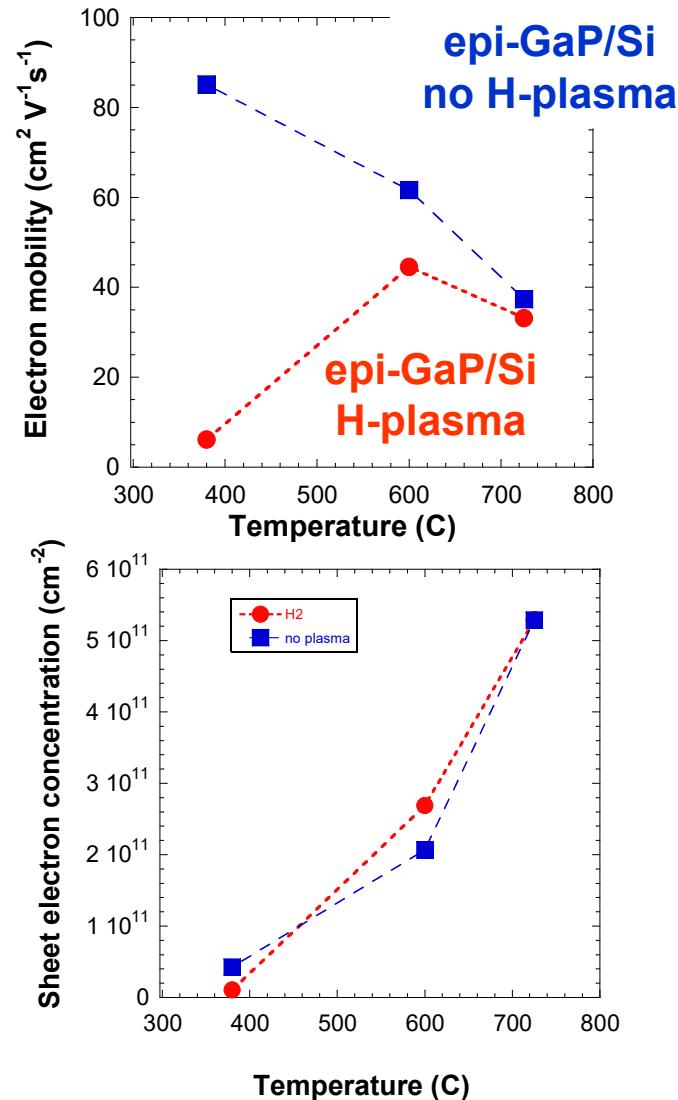
epi-GaP/Si



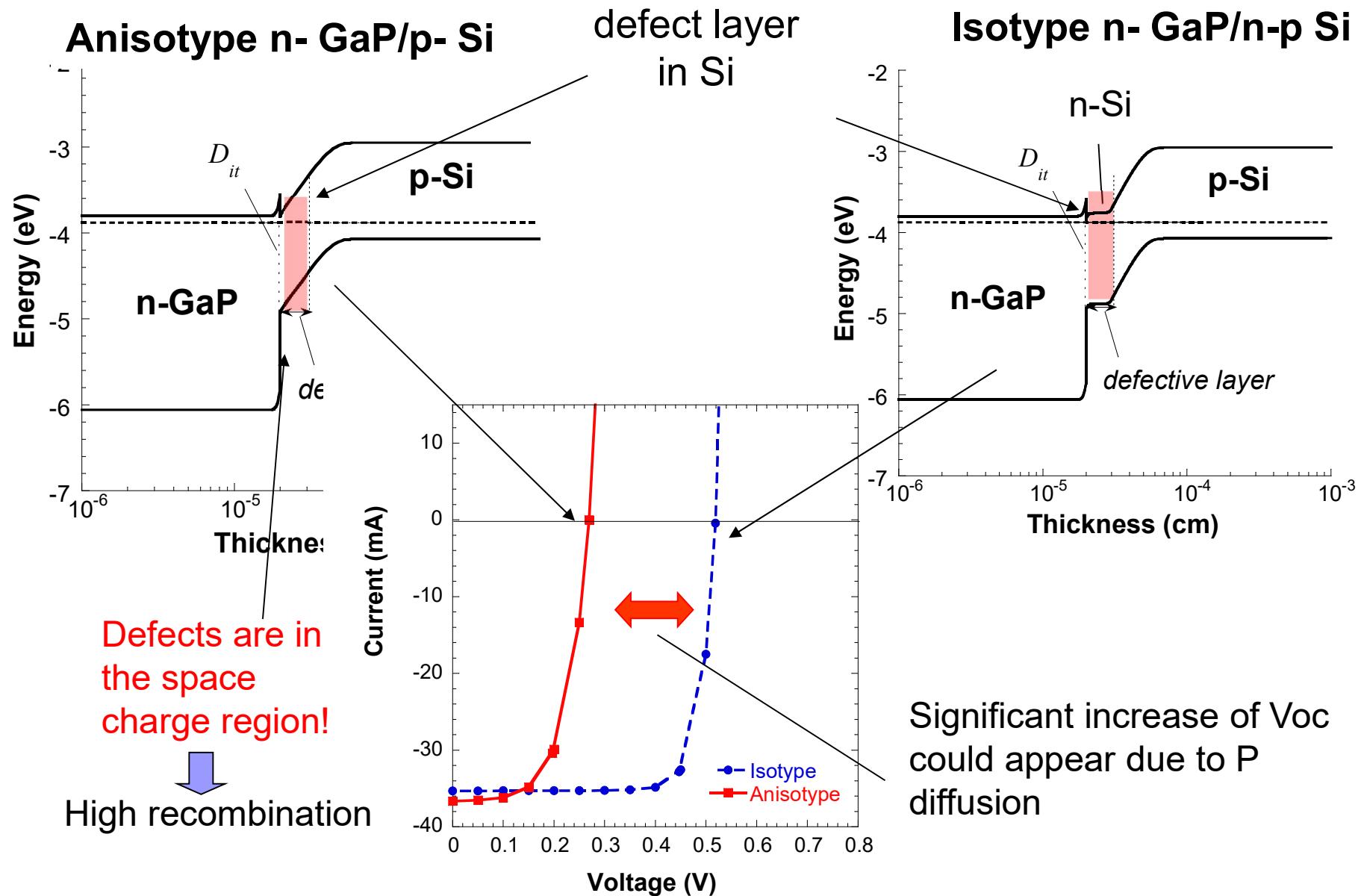
MOCVD chamber annealing (30 min, PH₃ environment)



The same trend as for RTA



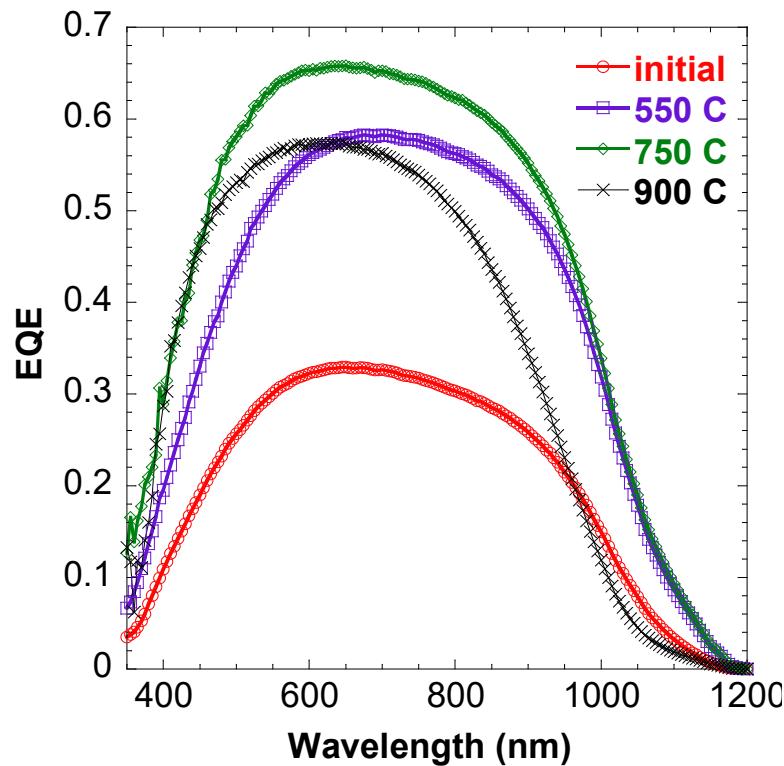
Simulations



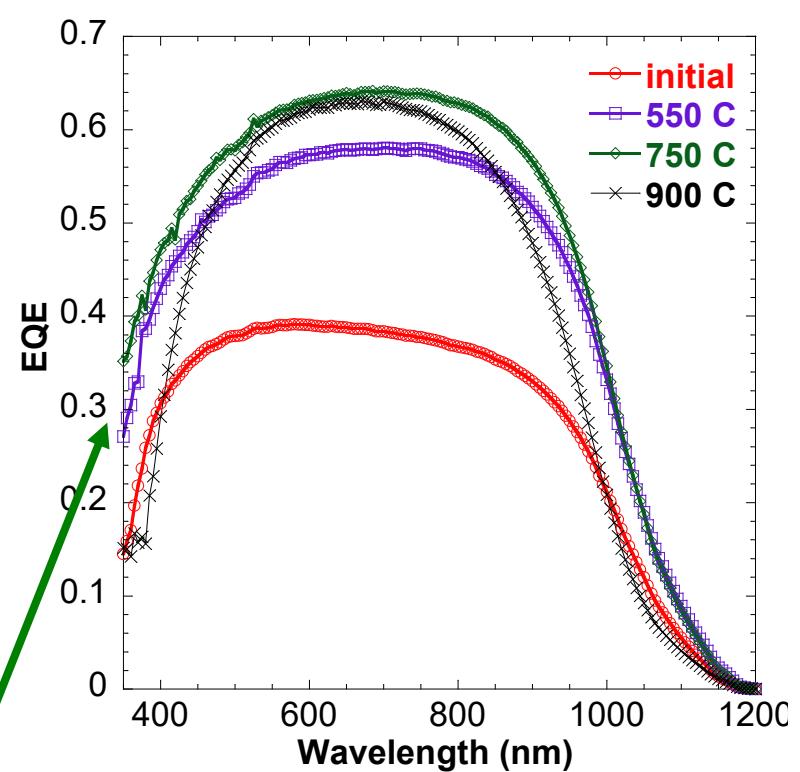
epi-GaP/Si

Quantum efficiency

epi-GaP/Si
H-plasma



epi-GaP/Si
no H-plasma



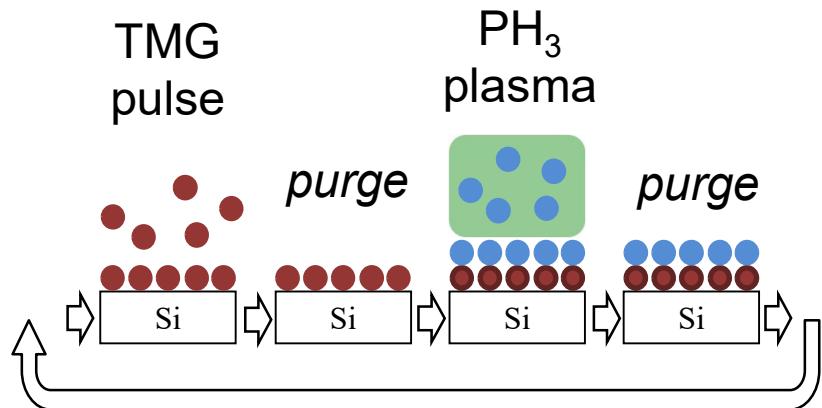
Higher efficiency at short wavelengths after annealing @ 550 and 750 °C

Lower recombination losses → Lower defect concentration

Process without H₂ plasma is preferable!

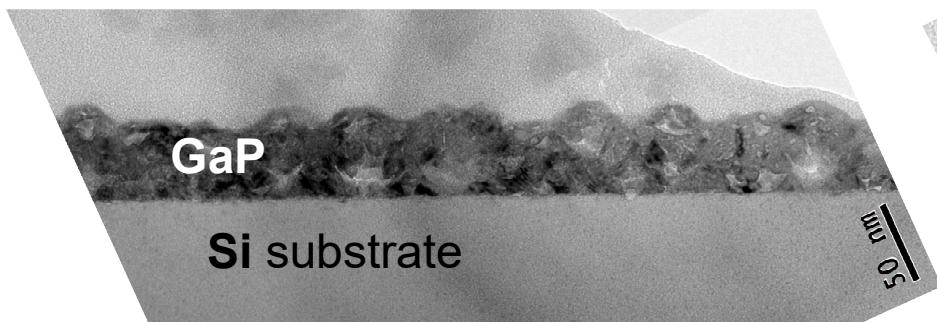
PE-ALD without H₂ plasma

No plasma activation

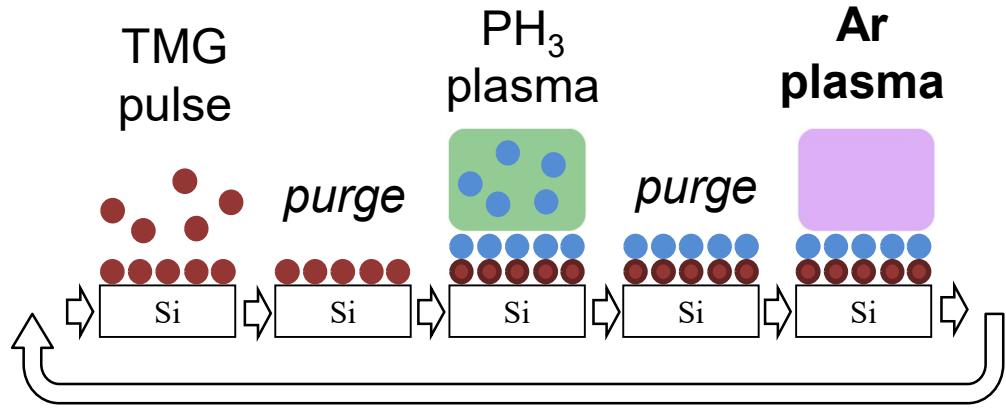


400 cycles

RMS = 3.76 nm

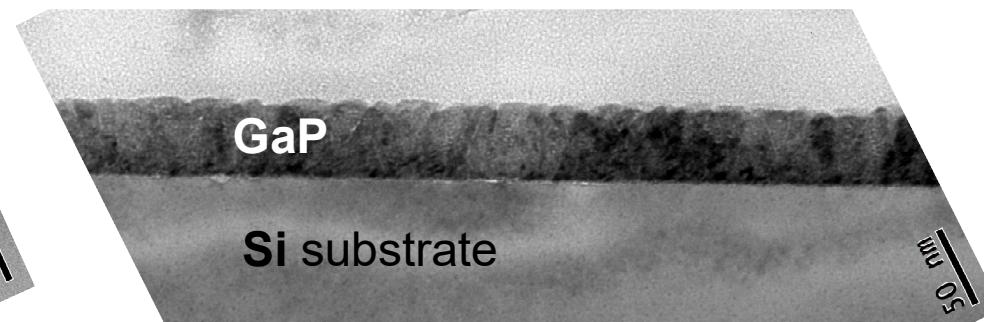


Ar plasma surface activation



200 cycles

RMS = 1.8 nm



First MOCVD growth of GaP on GaP/Si templates

Si substrates
cleaning by
the *Shiraki*
method

A. Ishizaka, Y.
Shiraki, J.
Electrochem. Soc.
1986, 133, 666

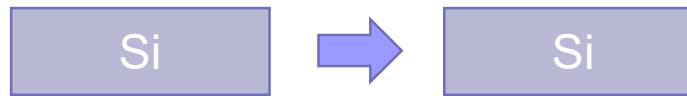
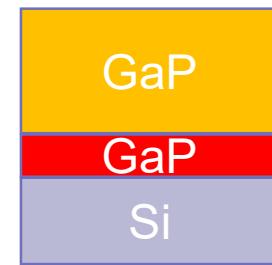
HF dip
(oxide removing,
H-passivation)

PE-ALD
of ultrathin
GaP
T 380 °C

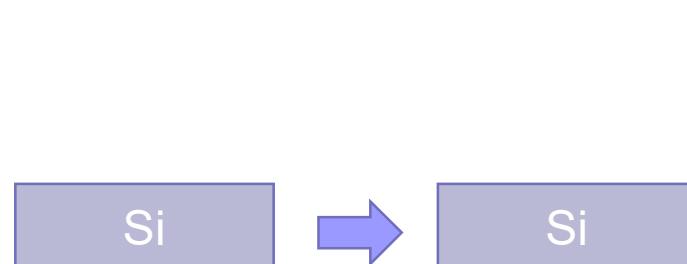
MOCVD growth
of GaP
Aixtron AIX 200/4
T 600 ...725 °C

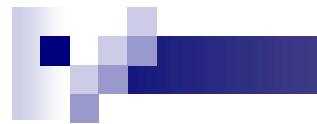
No plasma activation
30 cycles (3-5 nm)

Transfer
to MOCVD

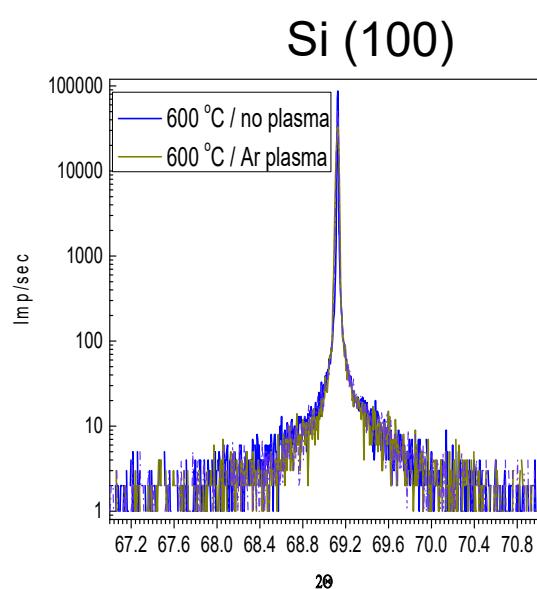


Ar plasma activation
20 cycles (3-5 nm)

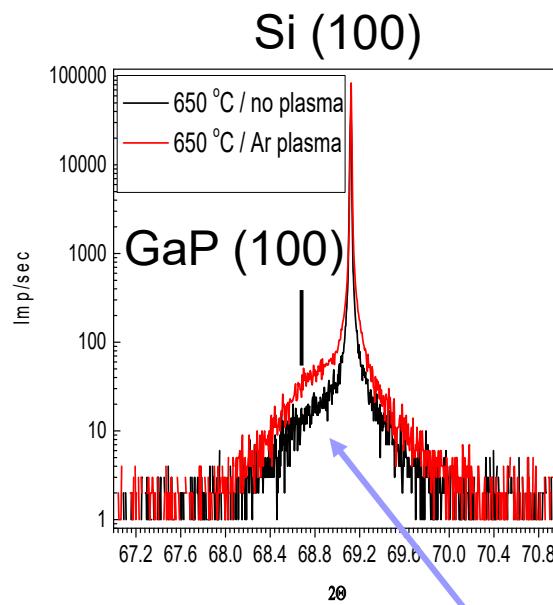




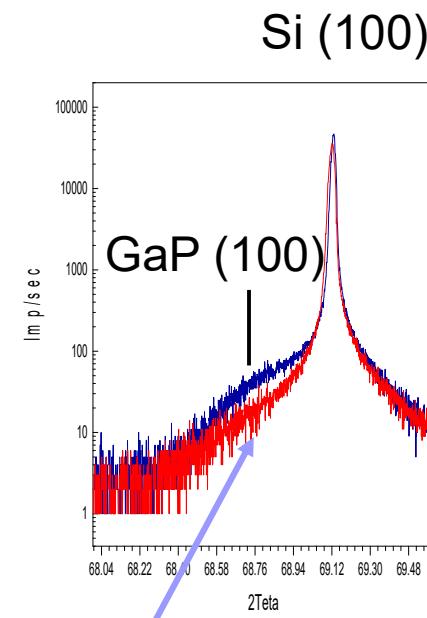
XRD for GaP grown on GaP/Si templates



MOCVD growth @ 600 °C



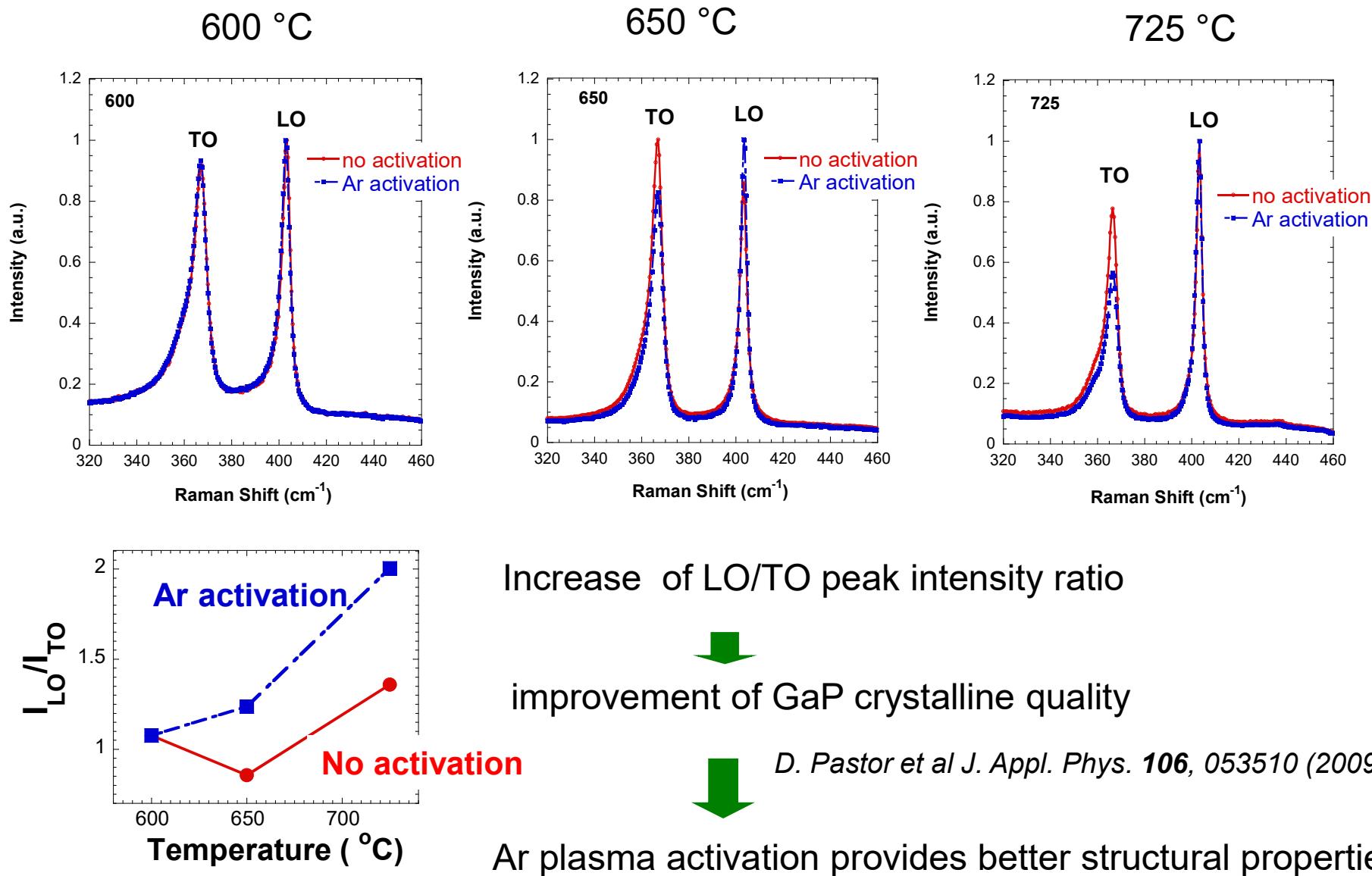
MOCVD growth @ 650 °C



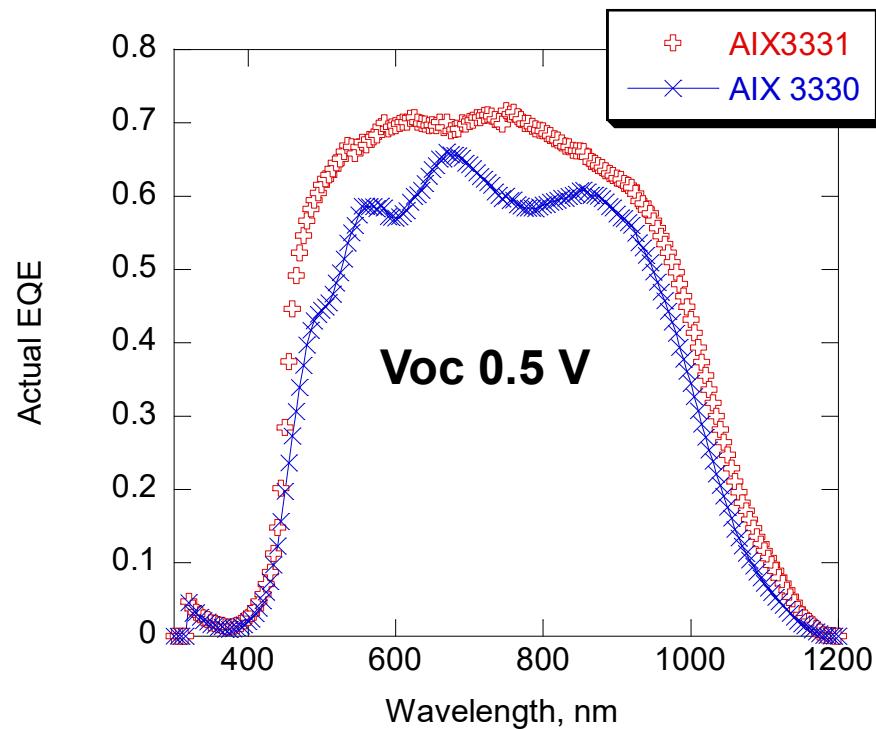
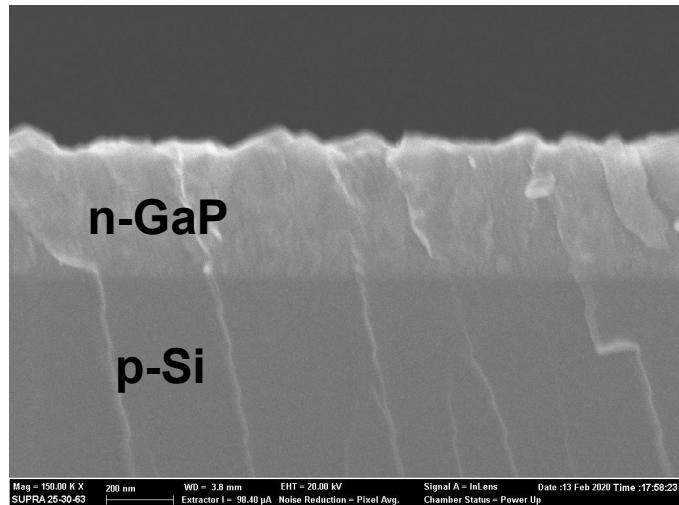
MOCVD growth @ 725 °C

Epitaxial growth was achieved!

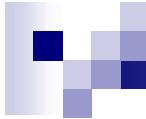
Raman spectra of GaP on Si



First n-GaP/p-Si test cell grown by MOCVD on GaP/Si template



epi-GaP/Si interface fabricated by PE-ALD is stable for further MOCVD growth of GaP at the temperature up to 750 °C



Conclusions

- **epi-GaP/Si interface** fabricated by PE-ALD are stable with temperature up to 750 C
- PE-ALD GaP **without H₂ plasma** provides better interface properties
- **Ar plasma surface activation** during PE-ALD could provide better crystalline properties for further GaP growth

**Thank you very much
for your attention!**

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